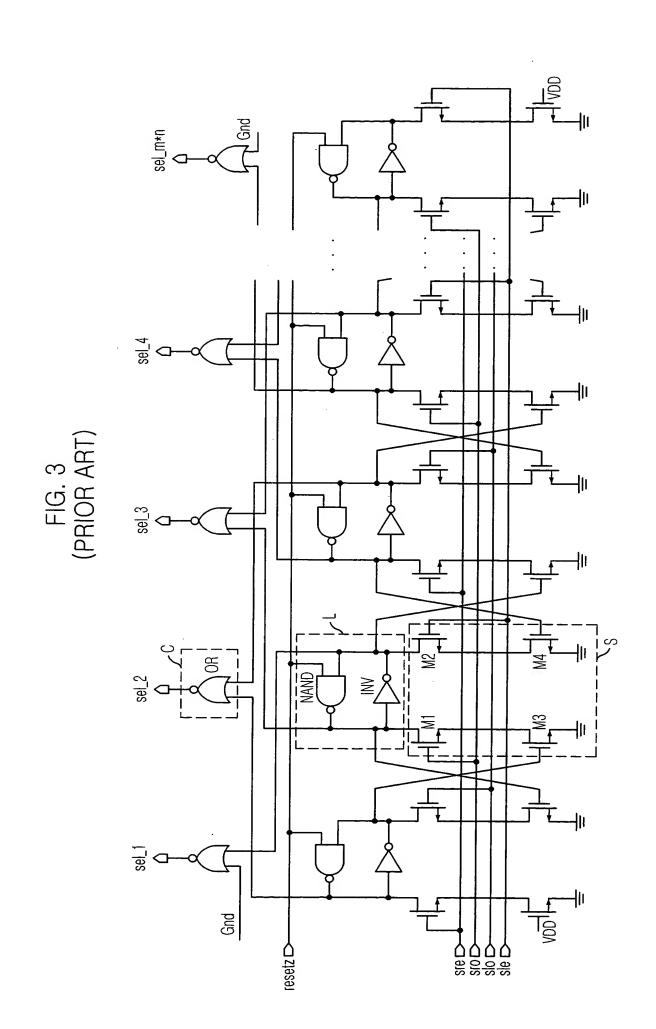
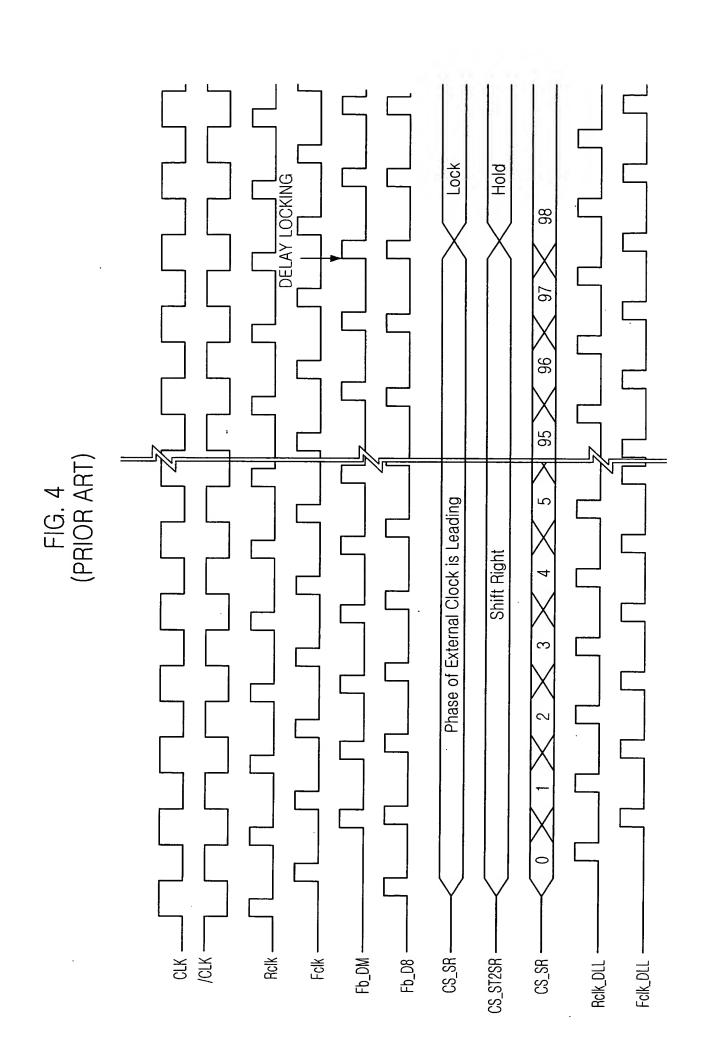


+ CIK_out / DU m*n NAND m*n DELAY UNIT sel_m*n CDU m+1 DELAY UNIT NAND 1 O sel_m+1 m No J NAND 101 DELAY UNIT NAND m+1 MDD NAND 100 sel_m NAND m-1 DELAY UNIT D sel_m-1 DELAY UNIT NAND 1 sel_01 00/

FIG. 2 (PRIOR ART)





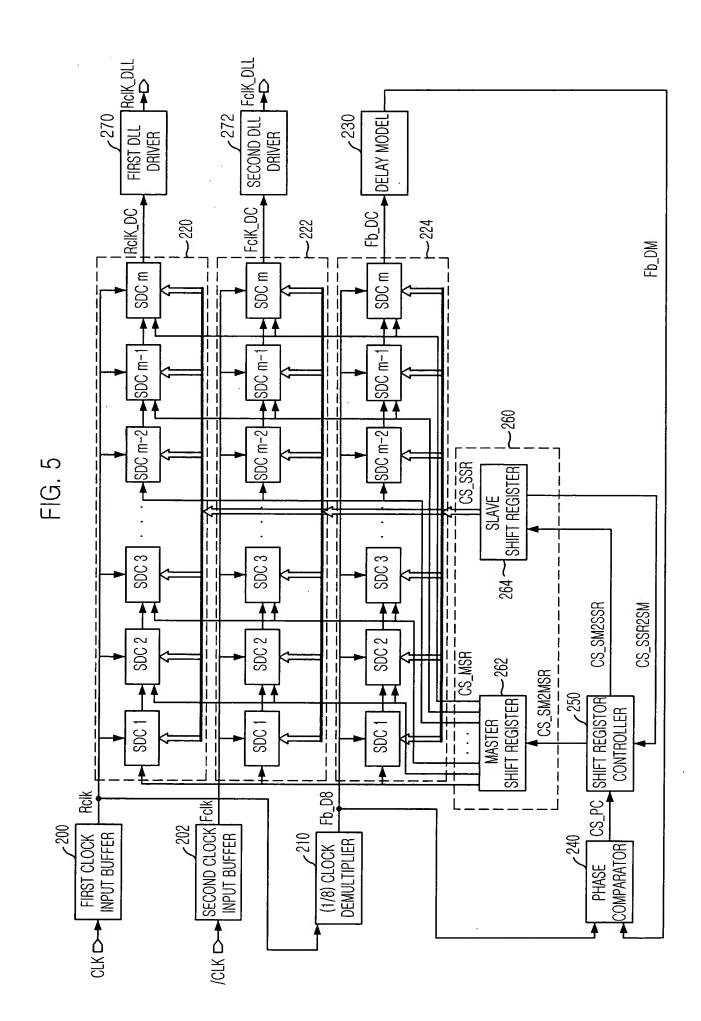
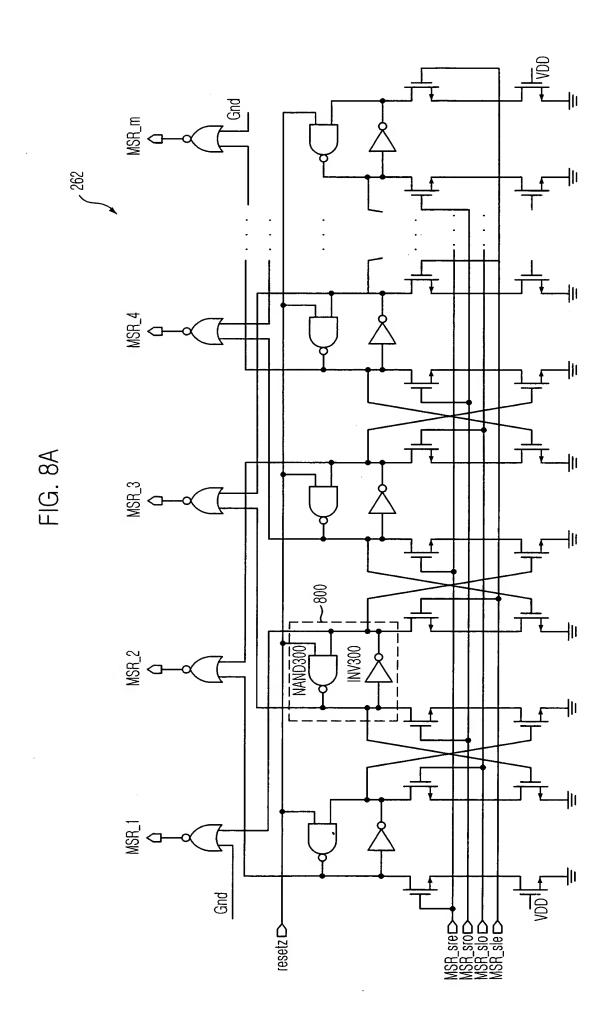


FIG. 6

FIG. 7



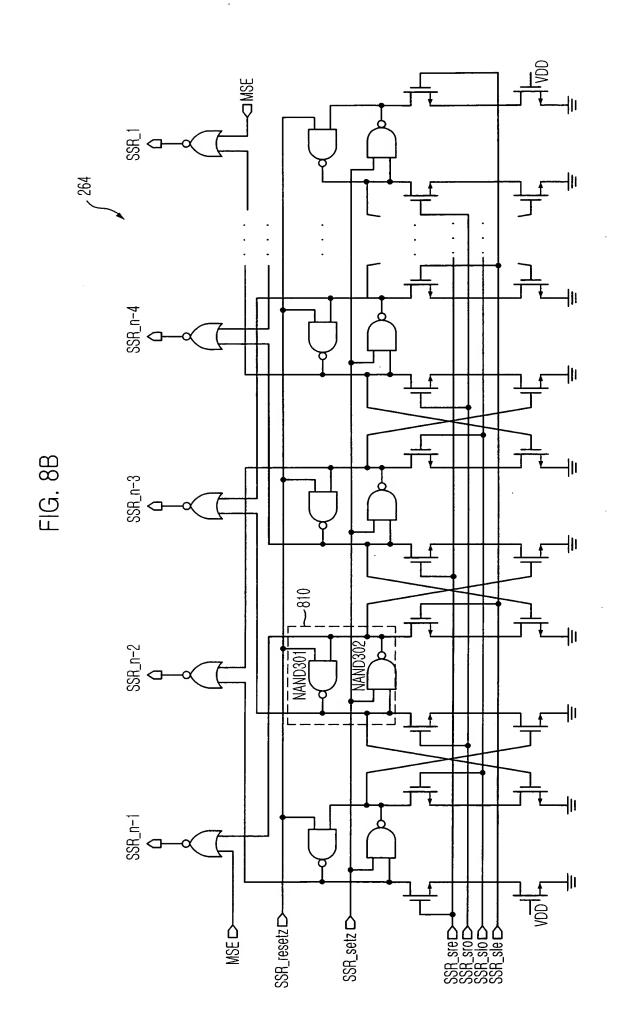


FIG. 9

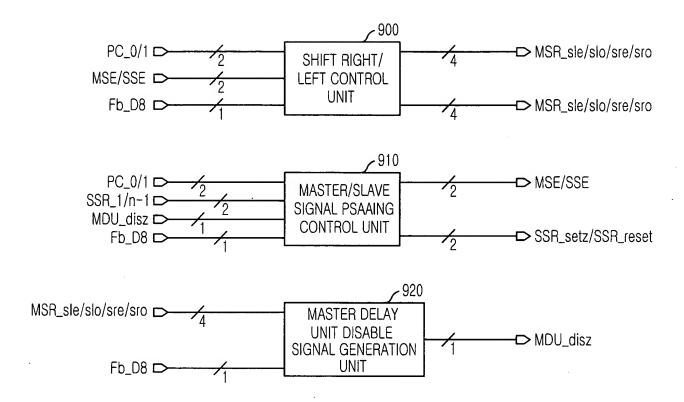
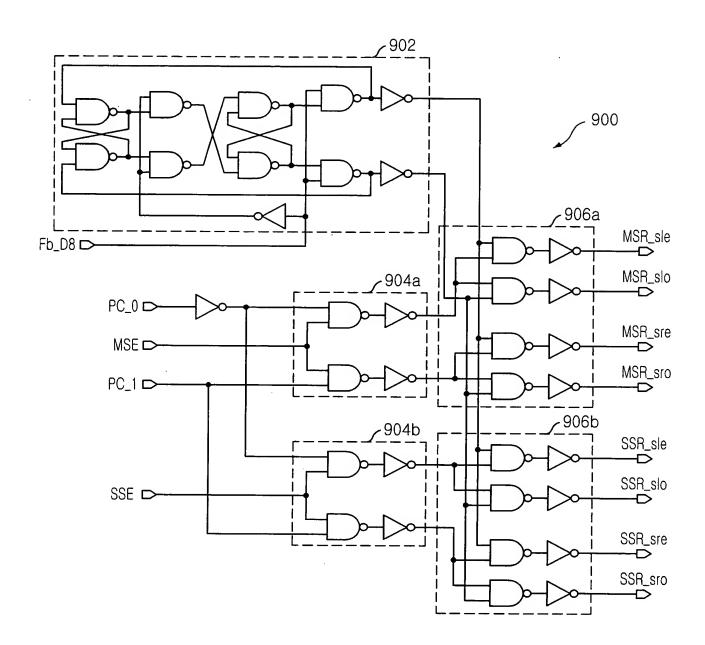
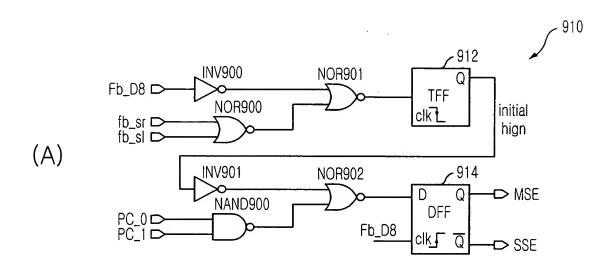
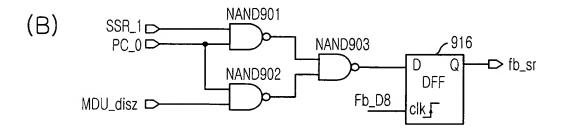
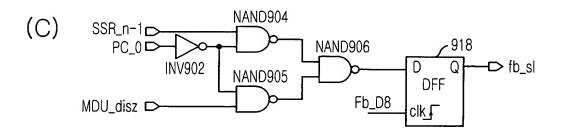


FIG. 10A









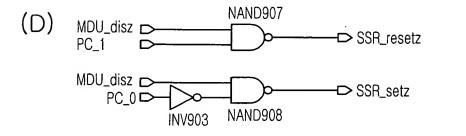


FIG. 10C

